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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Serge PONTAROLLO
Serial No.: 08/653,958
Filed: May 22, 1996
For: CLAMP

Examiner: LEJA, R.
Art Unit: 2836

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The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to Box Fee Amendment, Assistant Commissioner for Patents, Washington, D.C. 20231, on the 22nd day of September, 1998.

10/7/98
JC

James H. Morris
Reg. No.: 34,681

Assistant Commissioner For Patents
Washington, D.C. 20231

Sir:

AMENDMENT

In response to the Office Action mailed April 29, 1998, please amend the above-identified application as follows:

IN THE CLAIMS

Please cancel claims 7-13, 15, 17, 23 and 25 without prejudice or disclaimer.

Please amend claims 1 and 2 as follows:

1. (Twice Amended) A device for protecting a circuit against voltage surges,

09/29/1998 MAIL ROOM 08653958

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comprising
a MOS transistor of a first type directly connected to first and second supply terminals by its source and its drain, respectively, wherein said transistor of the first type shunts said voltage surges between said supply terminals in response to said voltage surges;

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